

HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	Microcomputer		No	TN-SH7-471A/E	Rev	1
THEME	SH7055F Hardware Manual Hardware Standby Mode		Classification of Information	1. Spec change ② Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	HD64F7055R		Lot No.	Reference Documents	Effective Date	
	HD64E7055				All lots	SH-2E SH7055F-ZTAT Hardware Manual ADE-602-155C Rev.4.0

Mode setup is needed in hardware standby mode. When signals are input with the combination that is not specified at mode set is set, transition to hardware standby will not made normally.

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24.3 Hardware Standby Mode

24.3.1 Transition to Hardware Standby Mode

Before Correction:

The chip enters hardware standby mode when the /HSTBY and /RST pins go low. Hardware standby mode reduces power consumption drastically by halting all SH7055 functions.

After Correction:

The chip enters hardware standby mode when the /HSTBY and /RST pins go low. Set the pins following to mode setup pin shown in [Section 4. Operation Mode]. Operation with other pin set up are not guaranteed. Hardware standby mode reduces power consumption drastically by halting all SH7055 functions.