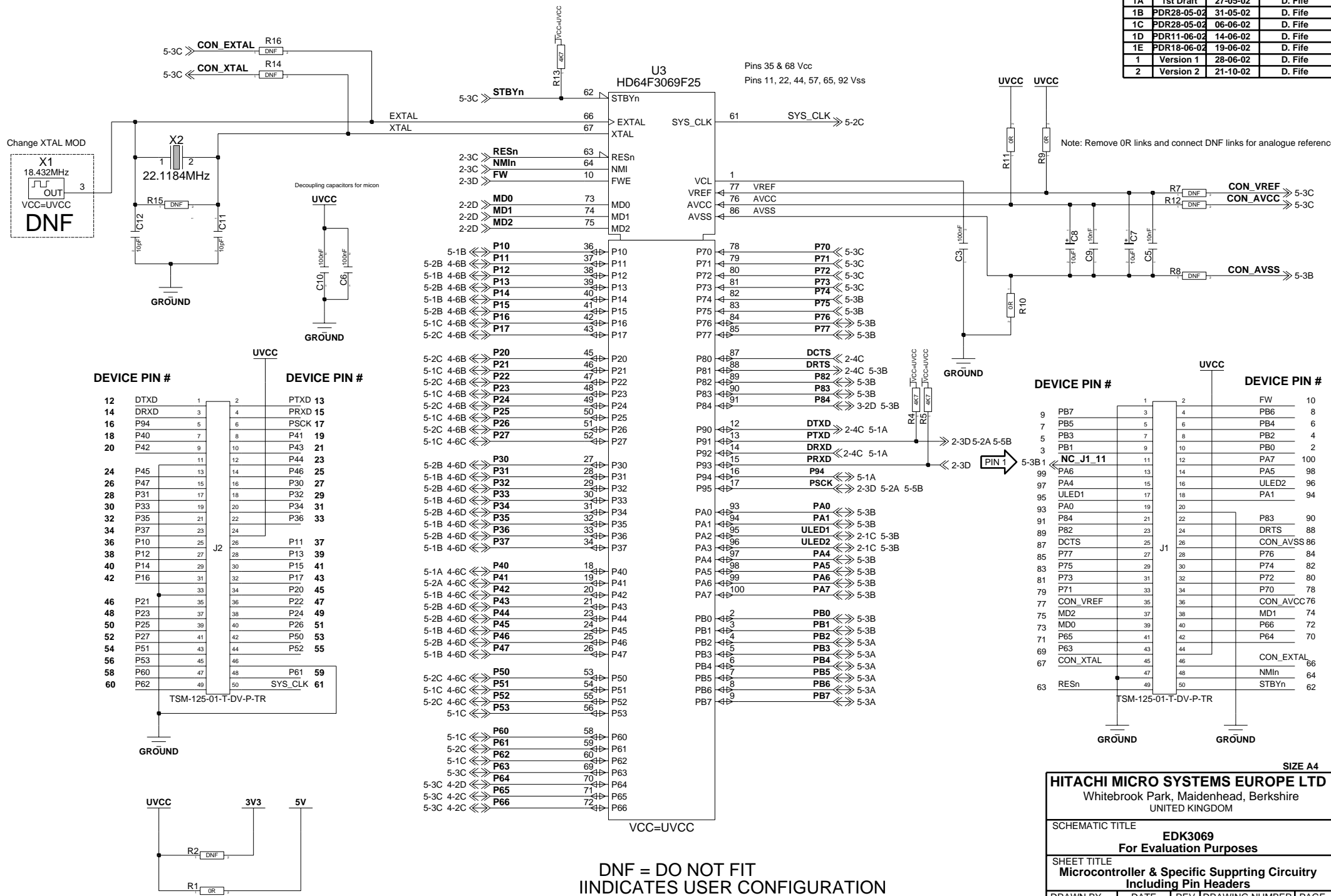
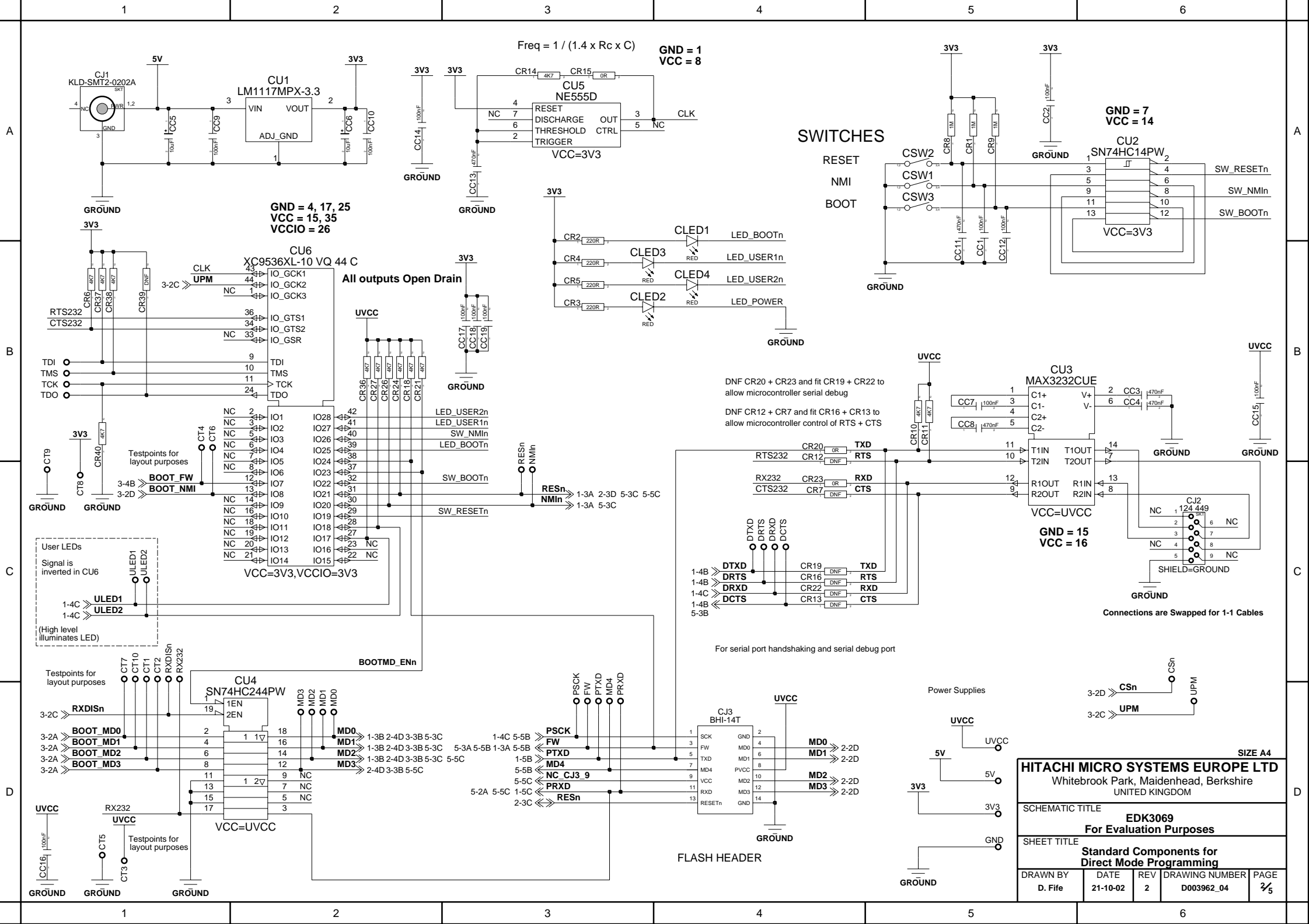
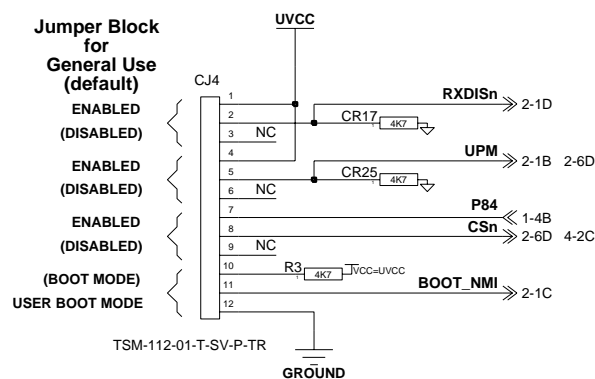
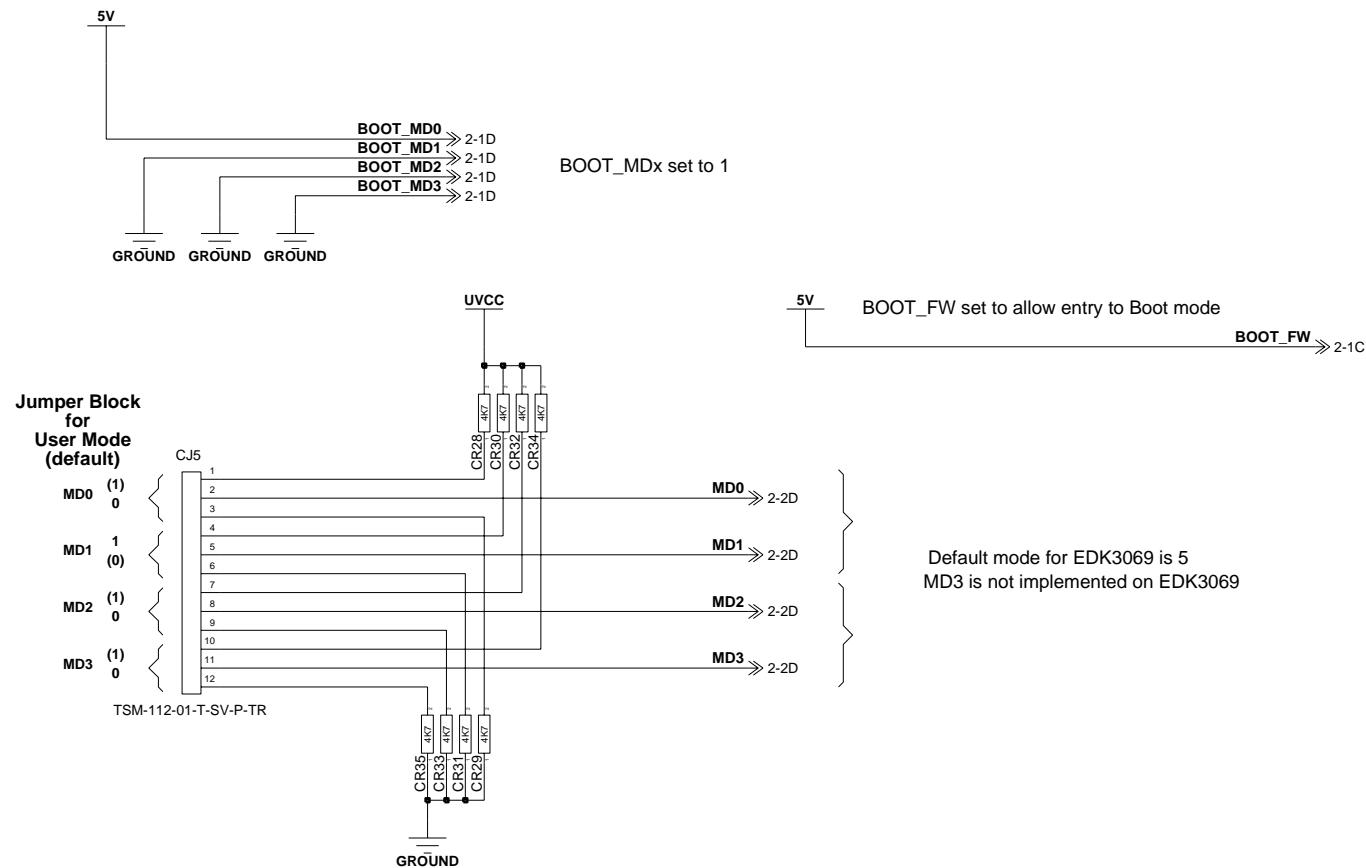


| REV | REF | DATE | DRAWN BY |
|-----|-------------|----------|----------|
| 1A | 1st Draft | 27-05-02 | D. Fife |
| 1B | PDR28-05-02 | 31-05-02 | D. Fife |
| 1C | PDR28-05-02 | 06-06-02 | D. Fife |
| 1D | PDR11-06-02 | 14-06-02 | D. Fife |
| 1E | PDR18-06-02 | 19-06-02 | D. Fife |
| 1 | Version 1 | 28-06-02 | D. Fife |
| 2 | Version 2 | 21-10-02 | D. Fife |







RXDISn

Normally flash header is disabled
Fit jumper to pins 1-2 to enable flash header and disable RS232 comms

USER_PGM

In case of FWE on device, input HIGH to tri-state the FW, causing User Program Mode transistion

CSn

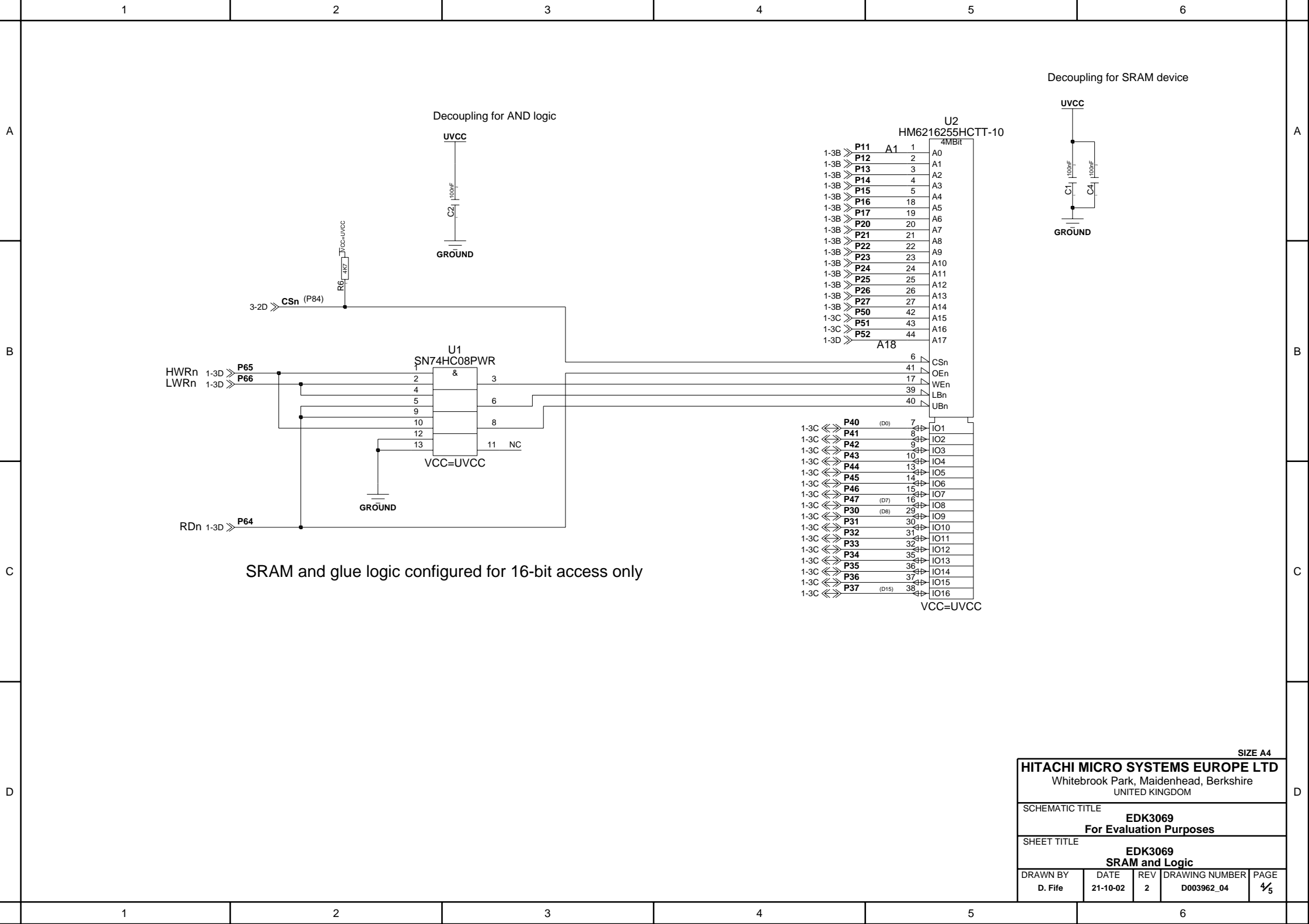
SRAM can be enabled or disabled via CS0n signal
By default the SRAM is disabled

User Boot

Fit Jumper 10-11 for Boot Mode
Fit jumper 11-12 for User Boot Mode

SIZE A4

| | | | | |
|-----------------------------------------------------------------------------------------------------|------------------|----------|------------------------------|-------------|
| HITACHI MICRO SYSTEMS EUROPE LTD Whitebrook Park, Maidenhead, Berkshire UNITED KINGDOM | | | | |
| SCHEMATIC TITLE | | | | |
| EDK3069 For Evaluation Purposes | | | | |
| SHEET TITLE | | | | |
| Programming and Config Headers | | | | |
| DRAWN BY D. Fife | DATE 21-10-02 | REV 2 | DRAWING NUMBER D003962_04 | PAGE 3/5 |



Microcontroller Expansion Connector Test Points

